

11/5/05

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention	A SUSPENSION FOR FILLING VIA HOLES IN SILICON AND METHOD FOR MAKING THE SAME
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Application Number : *10/707,693*
 Confirmation Number:
 First Named Applicant: JON CASEY
 Attorney Docket Number: FIS920030334US1
 Art Unit: *2823*
 Examiner: *F. TOLEDO*
 Search string: (3752685 or 5337475 or 5614043 or 6143421 or 6329065 or 6346317 or 6426126
).pn

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
<i>[initials]</i>	1	3752685	1973-08-14	Hidemasa Honda, et al.	<i>1</i>	<i>1</i>	
<i>[initials]</i>	2	5337475	1994-08-16	Aoude, et al.			
<i>[initials]</i>	3	5614043	1997-03-25	Ritland, et al.			
<i>[initials]</i>	4	6143421	2000-11-07	Ritland, et al.			
<i>[initials]</i>	5	6329065	2001-12-11	Ishida, et al.			
<i>[initials]</i>	6	6346317	2002-02-12	Ritland, et al.			
<i>[initials]</i>	7	6426126	2002-07-30	Conover, et al.	<i>1</i>	<i>1</i>	

Signature

Examiner Name	Date
<i>[Signature]</i>	<i>7/8/05</i>

Docket Number (Optional) FIS920030334US1	Application Number 10/707,693
Applicant(s) JON A. CASEY, ET AL.	
Filing Date 10/707,693	Group Art Unit 2823


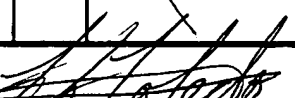
U.S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

[illegible]

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

		IBM TECHNICAL DISCLOSURE BULLETIN, "ACTIVE SILICON CHIP CARRIER" July 1972, PP 657-657	
EXAMINER 		DATE CONSIDERED 7/8/05	

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.